




## Freeform Search

---

<b>Database:</b>	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

<b>Term:</b>	(combin\$8 or mix\$3) near6 vector\$1 near7 scalar near7 instruction\$1	  
--------------	--	--

<b>Display:</b>	<input type="text" value="20"/>	<b>Documents in Display Format:</b>	<input type="text" value="TI"/>	<b>Starting with Number</b>	<input type="text" value="1"/>
-----------------	---------------------------------	-------------------------------------	---------------------------------	-----------------------------	--------------------------------

**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

---

Search

Clear

Interrupt

---

### Search History

---

**DATE:** Friday, February 27, 2004    [Printable Copy](#)    [Create Case](#)

#### Set Name Query

side by side

DB=USPT; PLUR=YES; OP=OR

#### Hit Count Set Name

result set

<u>L1</u>	(combin\$8 or mix\$3) near6 vector\$1 near7 scalar near7 instruction\$1	33	<u>L1</u>
-----------	---	----	-----------

END OF SEARCH HISTORY



Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 9. Document ID: US 6170001 B1

L1: Entry 9 of 33

File: USPT

Jan 2, 2001

US-PAT-NO: 6170001

DOCUMENT-IDENTIFIER: US 6170001 B1

TITLE: System for transferring format data from format register to memory wherein format data indicating the distribution of single or double precision data type in the register bank

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 10. Document ID: US 6061711 A

L1: Entry 10 of 33

File: USPT

May 9, 2000

US-PAT-NO: 6061711

DOCUMENT-IDENTIFIER: US 6061711 A

TITLE: Efficient context saving and restoring in a multi-tasking computing system environment

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 11. Document ID: US 6058465 A

L1: Entry 11 of 33

File: USPT

May 2, 2000

US-PAT-NO: 6058465

DOCUMENT-IDENTIFIER: US 6058465 A

TITLE: Single-instruction-multiple-data processing in a multimedia signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 12. Document ID: US 5898897 A

L1: Entry 12 of 33

File: USPT

Apr 27, 1999

US-PAT-NO: 5898897

DOCUMENT-IDENTIFIER: US 5898897 A

TITLE: Bit stream signal feature detection in a signal processing system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 13. Document ID: US 5838984 A

L1: Entry 13 of 33

File: USPT

Nov 17, 1998

US-PAT-NO: 5838984

DOCUMENT-IDENTIFIER: US 5838984 A

TITLE: Single-instruction-multiple-data processing using multiple banks of vector registers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	----------

☐ 14. Document ID: US 5821886 A

L1: Entry 14 of 33

File: USPT

Oct 13, 1998

US-PAT-NO: 5821886

DOCUMENT-IDENTIFIER: US 5821886 A

TITLE: Variable length code detection in a signal processing system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	----------

☐ 15. Document ID: US 5778241 A

L1: Entry 15 of 33

File: USPT

Jul 7, 1998

US-PAT-NO: 5778241

DOCUMENT-IDENTIFIER: US 5778241 A

TITLE: Space vector data path

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	----------

☐ 16. Document ID: US 5745721 A

L1: Entry 16 of 33

File: USPT

Apr 28, 1998

US-PAT-NO: 5745721

DOCUMENT-IDENTIFIER: US 5745721 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Partitioned addressing apparatus for vector/scalar registers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	----------

☐ 17. Document ID: US 5717881 A

L1: Entry 17 of 33

File: USPT

Feb 10, 1998

US-PAT-NO: 5717881

DOCUMENT-IDENTIFIER: US 5717881 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Data processing system for processing one and two parcel instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 18. Document ID: US 5706490 A

L1: Entry 18 of 33

File: USPT

Jan 6, 1998

US-PAT-NO: 5706490

DOCUMENT-IDENTIFIER: US 5706490 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Method of processing conditional branch instructions in scalar/vector processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 19. Document ID: US 5659706 A

L1: Entry 19 of 33

File: USPT

Aug 19, 1997

US-PAT-NO: 5659706

DOCUMENT-IDENTIFIER: US 5659706 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Vector/scalar processor with simultaneous processing and instruction cache filling

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 20. Document ID: US 5640524 A

L1: Entry 20 of 33

File: USPT

Jun 17, 1997

US-PAT-NO: 5640524

DOCUMENT-IDENTIFIER: US 5640524 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Method and apparatus for chaining vector instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Term	Documents
SCALAR	9823
SCALARS	875
COMBIN\$8	0
COMBIN	74
COMBINA	153
COMBINAATION	5
COMBINAATIONS	3
COMBINABILITY	99
COMBINABILTY	4
COMBINABLE	2476
COMBINABLENESS	2
((COMBIN\$8 OR MIX\$3) NEAR6 VECTOR\$1 NEAR7 SCALAR NEAR7 INSTRUCTION\$1).USPT.	33

[There are more results than shown above. Click here to view the entire set.](#)

Display Format:

[Change Format](#)

[Previous Page](#)

[Next Page](#)

[Go to Doc#](#)